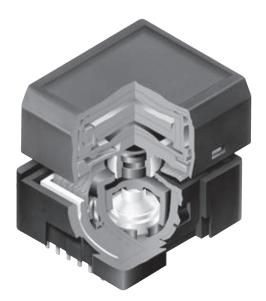
# <u>SmartSwitch™</u>

## **DISTINCTIVE CHARACTERISTICS**

## Standard with Enhanced LED Illumination:

- Broad and even light diffusion
- Consistent backlighting
- Low energy consumption

Programmable LCD Variety of LED Backlighting Colors Rubber Dome Epoxy Sealed Straight PC Terminals



RGB backlighting provides infinite color availability.

Programmable to display graphics, alphanumeric characters and animated sequences.

Integrated liquid crystal display provides wide viewing angle with high contrast and clarity.

Wider viewing area of 17.0mm x 13.0mm (horizontal x vertical) at 36 x 24 pixels.

Dome gives crisp tactile feedback to positively indicate circuit transfer.

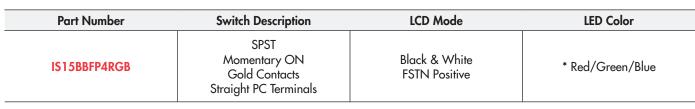
High reliability and long life of one million actuations minimum.

Epoxy sealed terminals prevent entry of solder flux and other contaminants.

Optional accessories available to enhance panel design and simplify production process.



## PART NUMBER & DESCRIPTION



\* Simultaneous RGB illumination achieves infinite colors.

Rev 04.01.15 • 10.14.16 • 12.29.17

ATTENTION

ELECTROSTATIC SENSITIVE DEVICES



## SWITCH SPECIFICATIONS

## LCD SPECIFICATIONS

**Display Operation Mode** 

**Characteristics of Display** 

Circuit	SPST normally open
Electrical Capacity (Resistive Load)	100mA @ 12V DC
Contact Resistance	200 milliohms max @ 20mV 10mA
Insulation Resistance	100 megohms min @ 100V DC
Dielectric Strength	125V AC for 1 minute minimum
Mechanical Endurance	1,000,000 operations minimum
Electrical Endurance	1,000,000 operations minimum
Operating Force	2.2 ± 0.5 Newtons
Total Travel	1.8mm (.071")
Operating Temp. Range	−20°C ~ +60°C (−4°F ~ +140°F)
Storage Temp. Range	−30°C ~ +70°C (−22 °F ~ +158°F)

Absolute Maximum Ratings (Temperature at 25°C)

**Symbols** 

 $V_{DD}$ 

 $V_{\text{LC}}$ 

 $V_{\rm I}$ 

 $V_{\rm O}$ 

Items

**Supply Voltage for Logics** 

Supply Voltage for LCD

Input Voltage

**Output Voltage** 

Display Condition	Transflective with built-in LED backlight						
Viewing Angle	Viewing Angle 6 o'clock						
Driving Method	Aethod 1/24 duty. 1/5 bias (built-in driving circuit)						
Viewing Area	17.0mm	x 13.0	mm (horizontal x ve	rtical)			
Pixel Format	36 x 24 p	oixels (	horizontal x vertical	)			
Pixel Size	Pixel Size 0.440mm x 0.495mm (horizontal x vertical)						
Backlight LED	Backlight LED RGB: red/green/blue						
Recommended Operating Conditions (Temperature at 25°C)							
ltems S	ymbols Mi	nimum	Typical	Maximum			
Supply Voltage for Logics	Supply Voltage for Logics V <sub>DD</sub> 3.0V - 5.5V						
Supply Voltage Black/White V <sub>LC</sub> — * 7.3V —							
Input Voltage	V <sub>1</sub> OV — V <sub>DD</sub>						
Driving Frequency f <sub>FLM</sub> — 150Hz: black/white —							

FSTN positive

\* LCD voltage (V<sub>LC</sub>) level depends on refreshing frequency and temperature. The optimal V<sub>LC</sub> can differ slightly from the stated typical value.

### DC Characteristics of LCD Drive (Temperature at $-20^{\circ}$ C to $+60^{\circ}$ C and $V_{DD} = 5.0V \pm 10\%$ )

Ratings -0.3V to +7.0V

-0.3V to +12.0V

–0.3V to  $V_{\text{DD}}$  +0.3V

–0.3V to  $V_{\mbox{\scriptsize DD}}$  +0.3V

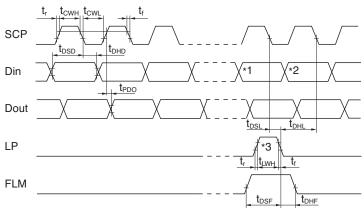
ltems	Symbols	Test Conditions	Minimum	Typical	Maximum	Unit
High Level Input Voltage	V <sub>IH</sub>		0.7V <sub>DD</sub>		V <sub>DD</sub>	V
Low Level Input Voltage	V <sub>IL</sub>		0		$0.3V_{\text{DD}}$	V
High Level Input Leakage Current	I <sub>UH</sub>	$V_{I} = V_{DD}$			10	μA
Low Level Input Leakage Current	ILIL	$V_1 = 0V$			-10	μA
High Level Output Voltage	V <sub>OH</sub>	I <sub>OH</sub> = -500μA	V <sub>DD</sub> -0.5			V
Low Level Output Voltage	V <sub>OL</sub>	I <sub>OL</sub> = 500μA			0.5	V
High Level Output Leakage Current	I <sub>LOH</sub>	$V_{\rm O} = V_{\rm DD}$			10	μA
Low Level Output Leakage Current	ILOL	$V_{\odot} = 0V$			-10	μA
Supply Current	I <sub>DD</sub>	$f_{SCP} = 1.0MHz$			500	μA
LCD Drive Current	ILC	$f_{LP} = 2.4 \text{kHz} V_{LC} = 7.3 \text{V}$		500	2,000	μA

#### Timing Characteristics of LCD Drive IC

(Temperature at $-20^{\circ}$ C to $+60^{\circ}$ C and $V_{DD} = 5.0^{\circ}$	V ±10%)
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Symbols	Minimum	Maximum
f <sub>SCP</sub>		8.0MHz
f <sub>LP</sub>		50kHz
t <sub>CWH</sub>	50ns	
t <sub>CWL</sub>	50ns	
t <sub>DSD</sub>	45ns	
t <sub>DHD</sub>	50ns	
t <sub>PDO</sub>		25ns
t <sub>DSL</sub>	50ns	
t <sub>DHL</sub>	50ns	
t <sub>LWH</sub>	50ns	
t <sub>DSF</sub>	50ns	
t <sub>DHF</sub>	50ns	
t <sub>r</sub> /t <sub>f</sub>		15ns
	fscp   fLP   tcwh   tcwl   tbsb   tbbb   tbbb	f <sub>SCP</sub> f <sub>LP</sub> t <sub>CWH</sub> 50ns   t <sub>CWL</sub> 50ns   t <sub>DSD</sub> 45ns   t <sub>DHD</sub> 50ns   t <sub>DSL</sub> 50ns   t <sub>DHL</sub> 50ns   t <sub>DSF</sub> 50ns   t <sub>DSF</sub> 50ns   t <sub>DSF</sub> 50ns

#### **Timing Diagram**

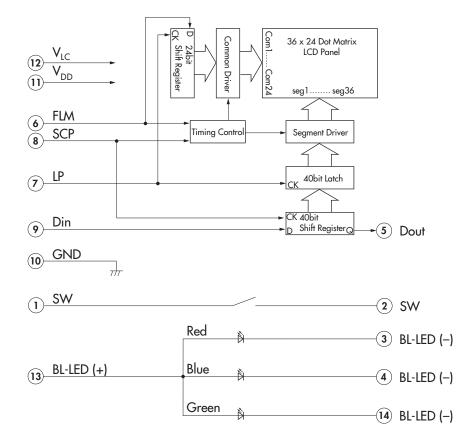


- \*1 Last data on first line
- \*2 Beginning data on second line
- \*3 Location of LP signal on first line





IS15BBFP4RGB RGB LED Backlight Black and White LCD



<u>Pin No.</u>	<u>Symbol</u>	Name	Function
	SW	Terminal of Switch	Normally open
2	SW	Terminal of Switch	Normally open
3	BL-LED ()	Terminal of Backlight LED	Cathode for red
(4)	BL-LED ()	Terminal of Backlight LED	Cathode for blue
5	Dout	Data Output	Display serial output. Can be used to connect to Din of the next SMARTSWITCH. As a result, many SMARTSWITCHES can be controlled with one clock and data signal.
6	FLM	First Line Marker	The marking signal for the first line data of LCD display. The first line of LCD will be selected by the falling edge of LP signal during the high level (FLM).
7	LP	Latch Pulse	Line data latch pulse will latch content of internal 40-bit shift register at falling edge for one line of display. LP will also increment the display line by one.
8	SCP	Serial Clock Pulse	Clock used by 40-bit internal shift register of the switch, shifting the display data bit presented at Din at falling edge.
9	Din	Data Input	Display serial data bit. Note: to map the display data, because of the difference between the number of internal shift register data (40) and the single line of LCD pixels (36), the first four bits of data shifted will be dummy bits.
10	GND	Ground	
11	V <sub>DD</sub>	Power	Power source for logic circuit
(12)	$V_{LC}$	Power	Power source for LCD drive
13	BL-LED (+)	Terminal of Backlight LED	Anode for common
14	BL-LED ()	Terminal of Backlight LED	Cathode for green



# SUPER BRIGHT RGB LED SPECIFICATIONS

Typical	<b>Electrical</b>	<b>Characteristics</b>	(Temperature at 25°C)
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Backlight Color	Symbols	Red	Green	Blue	Unit
Forward Current	I <sub>F</sub>	10	8.5	8.0	mA
Forward Voltage	V <sub>F</sub>	2.0	2.8	2.8	V

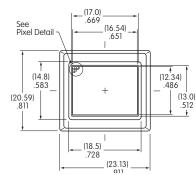
## ABSOLUTE MAXIMUM FOR RGB LED

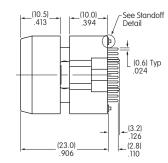
#### Electrical Characteristics (Temperature at 25°C)

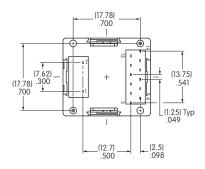
Backlight Color	Symbols	Red	Green	Blue	Unit
Forward Current	I <sub>F</sub>	20	20	20	mA
Forward Voltage	V <sub>F</sub>	2.0 (I <sub>F</sub> = 10mA)	2.8 (I <sub>F</sub> = 8.5mA)	2.8 (I <sub>F</sub> = 8.0mA)	V
Reverse Voltage	V <sub>R</sub>	4.0	4.0	4.0	V
Current Reduction Rate Above 25°C	∆I <sub>F</sub> (DC)	-0.33	-0.33	-0.33	mA/°C
* <b>Power Dissipation</b> (LED Overall 115mW)	P <sub>D</sub>	40	60	60	mW

\*For uniform light emission, Power Dissipation should not exceed the Absolute Maximum Rating, and the Forward Current should not exceed the derated Absolute Forward Current.

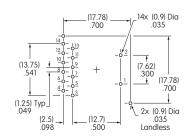
## TYPICAL SWITCH DIMENSIONS WITH RGB LED



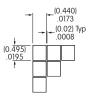




Terminal numbers are not on the switch.



Footprint



**Pixel Detail** 

(0.6) (0.3) .012 (0.75) Dia .030 (1.8) Dia .071

**Standoff Detail** 



## PRECAUTIONS FOR HANDLING & STORAGE OF LCD 36 x 24 DEVICES

#### Handling

- 1. The IS Series devices are electrostatic sensitive.
- 2. Limit operating force to keytop to 100.0N maximum, as excessive pressure may damage the LCD device.
- 3. The IS series devices are not process sealed.
- 4. If the LCD is accidentally broken, avoid contact with the liquid and wash off any liquid spills to the skin or clothing.
- 5. Clean cap surface with dry cloth. If further cleaning is needed, wipe with dampened cloth using neutral cleanser and dry with clean cloth. Do not use organic solvent.
- 6. Recommended soldering time and temperature limits:

Do not exceed 70°C at the LCD level. Wave Soldering: see Profile B in the Supplement section. Manual Soldering for Switch: see Profile A in the Supplement section. Manual Soldering for Display: see Profile B in the Supplement section.

- 7. Recommendation for backlight color uniformity: Use constant current driver. For current limiting resistor method, the power source should be at least twice the backlight LED forward voltage.
- 8. The VLC voltage should not be applied before logic voltage. If VLC voltage is present before logic voltage, it may cause the driver logic to freeze and damage the LCD, and the driver logic may become damaged.
- 9. Backlight Forward Current should not exceed the derated Absolute Maximum Forward Current based on the temperature.
- 10. Excessive images may result after the same image is emitted continuously for an extended period of time.

#### Storage

- 1. Store in original container and away from direct sunlight.
- 2. Keep away from static electricity.
- 3. Avoid extreme temperatures, high humidity, gaseous substances, and all forms of chemical contamination.



